Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"6469632"	USPAT	OR	OFF	2003/12/08 14:18
S2	77889	test ADJ access ADJ port or (TAP)	USPAT	OR	OFF	2003/12/08 14:15
S3	77696	"714"/$.ccls. and test ADJ access ADJ port or (TAP)	USPAT	OR	OFF	2003/12/08 14:16
S4	77696	"714"/$.ccls. and (test ADJ access ADJ port) or (TAP)	USPAT	OR	OFF	2003/12/08 14:16
S5	77696	"714"/$.ccls. and (test ADJ access ADJ port) or TAP	USPAT	OR	OFF	2003/12/08 14:25
S6	79789	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) ands INEL.as.	USPAT	OR	OFF	2003/12/08 14:18
S7	87114	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) ands INTEL.as.	USPAT	OR	OFF	2003/12/08 14:19
S8	235	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and INTEL.as.	USPAT	OR	OFF	2003/12/08 14:19
S9	7531	Intel ADJ Corporation.as.	USPAT	OR	OFF	2003/12/08 14:20
S10	234	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and Intel ADJ Corporation.as.	USPAT	OR	OFF	2003/12/08 14:30
S11	427390	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and Intel ADJ Corporation.as. amd memory	USPAT	OR	OFF	2003/12/08 14:22
S12	164	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and Intel ADJ Corporation.as. and memory	USPAT	OR	OFF	2003/12/08 14:22
S13	89192	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) wit controller and Intel ADJ Corporation.as. and memory and seelct and conroller	USPAT	OR	OFF	2003/12/08 14:22
S14	89193	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) wit controller and Intel ADJ Corporation.as. and memory and select	USPAT	OR	OFF	2003/12/08 14:22
S15	39	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and controller and Intel ADJ Corporation.as. and memory and select	USPAT	OR	OFF	2003/12/08 14:24
S16	3228	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and controller and memory and select	USPAT	OR	OFF	2003/12/08 14:24

S17	1684	"714"/$.ccls. and (test ADJ access ADJ port) or TAP with controller	USPAT	OR	OFF	2003/12/08 14:26
S18	487	"714"/$.ccls. and (test ADJ access ADJ port) or TAP with controller and Intel adj Corporartion.as.	USPAT	OR	OFF	2003/12/08 14:27
S19	487	"714"/$.ccls. and test ADJ access ADJ port or TAP with controller and Intel adj Corporartion.as.	USPAT	OR	OFF	2003/12/08 14:35
S20	49	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and Intel ADJ Corporation.as. and core	USPAT	OR	OFF	2003/12/08 14:31
S21	164	("714"/$.ccls. and (test ADJ access ADJ port) or TAP) and Intel ADJ Corporation.as. and memory	USPAT	OR	OFF	2003/12/08 14:35
S22	1684	"714"/$.ccls. and test ADJ access ADJ port or TAP with controller	USPAT	OR	OFF	2003/12/08 14:37
S23	1684	"714"/$.ccls. and (test ADJ access ADJ port) or TAP with controller	USPAT	OR	OFF	2003/12/08 14:37
S24	341	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with controller	USPAT	OR	OFF	2003/12/08 14:40
S25	100	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with controller and memory with select$3	USPAT	OR	OFF	2003/12/08 14:41
S26	230	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with JTAG	USPAT	OR	OFF	2003/12/08 14:41
S27	201	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with JTAG and memory	USPAT	OR	OFF	2003/12/08 14:42
S28	201	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with JTAG and memory	USPAT	OR	OFF	2003/12/08 14:44
S29	127	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with JTAG and core	USPAT	OR	OFF	2003/12/08 14:47
S30	77	"714"/$.ccls. and (test ADJ access ADJ port or TAP) with JTAG and core and multiplexer	USPAT	OR	ON	2003/12/08 14:49
S31	47	"714"/$.ccls. and (test ADJ access ADJ port or TAP) near3 JTAG and core and multiplexer	USPAT	OR	ON	2003/12/08 14:51
S32	1	"714"/$.ccls. and (test ADJ access ADJ port or TAP) near3 JTAG and core and multiplexer and "IEEE-1149.1"	USPAT	OR	ON	2003/12/08 14:51
S33	8	"714"/$.ccls. and "IEEE-1149.1"	USPAT	OR	ON	2003/12/08 14:57
S34	4	("5396170" | "5497378" | "5687179" | "5708773").PN.	USPAT	OR	OFF	2003/12/08 14:56

S35	271	714/727.ccls.	USPAT	OR	ON	2003/12/08 14:59
S36	177	714/727.ccls. and multiplexer	USPAT	OR	ON	2003/12/08 15:01
S37	797	714/727.ccls. and multiplexer and core TAP near3 controller	USPAT	OR	ON	2003/12/08 15:02
S38	13	714/727.ccls. and multiplexer and core with TAP near3 controller	USPAT	OR	ON	2003/12/08 15:03
S39	30	"714"/$.ccls. and multiplexer and core with TAP near3 controller	USPAT	OR	ON	2003/12/08 15:17
S40	30	"714"/$.ccls. and multiplexer and core with TAP near3 controller and data and select and clock	USPAT	OR	ON	2003/12/08 15:05
S41	16	"714"/$.ccls. and multiplexer and core with TAP near3 controller and data with select with clock	USPAT	OR	ON	2003/12/08 15:13
S42	16	"714"/$.ccls. and multiplexer and core with TAP with controller and data with select with clock	USPAT	OR	ON	2003/12/08 15:13
S43	30	"714"/$.ccls. and multiplexer and core with TAP with controller	USPAT	OR	ON	2003/12/08 15:13
S44	72	"714"/$.ccls. and multiplexer with TAP near3 controller	USPAT	OR	ON	2003/12/08 15:27
S45	47	"714"/$.ccls. and multiplexer with TAP near3 controller and JTAG	USPAT	OR	ON	2004/05/10 16:54
S46	14	"714"/$.ccls. and multiplexer near3 TAP and JTAG	USPAT	OR	ON	2003/12/08 15:41
S47	1	"714"/$.ccls. and multiplexer near3 TAP with core	USPAT	OR	ON	2003/12/08 15:41
S48	47	"714"/$.ccls. and multiplexer near3 TAP	USPAT	OR	ON	2003/12/08 15:51
S49	14	"714"/$.ccls. and multiplexer near3 TAP and core	USPAT	OR	ON	2004/05/10 16:10
S50	4	("5396170" | "5497378" | "5687179" | "5708773").PN.	USPAT	OR	OFF	2003/12/08 16:08
S51	6	"6324662".URPN.	USPAT	OR	OFF	2003/12/08 16:09
S52	52	"714"/$.ccls. and multiplexer and TAP with core	USPAT	OR	ON	2004/05/10 16:10
S53	1	"20030046625"	US-PGPUB; USPAT	OR	OFF	2004/12/10 14:32
S54	1	US-6334198-$.DID.	USPAT	OR	OFF	2004/12/10 14:32